Best Available Copy

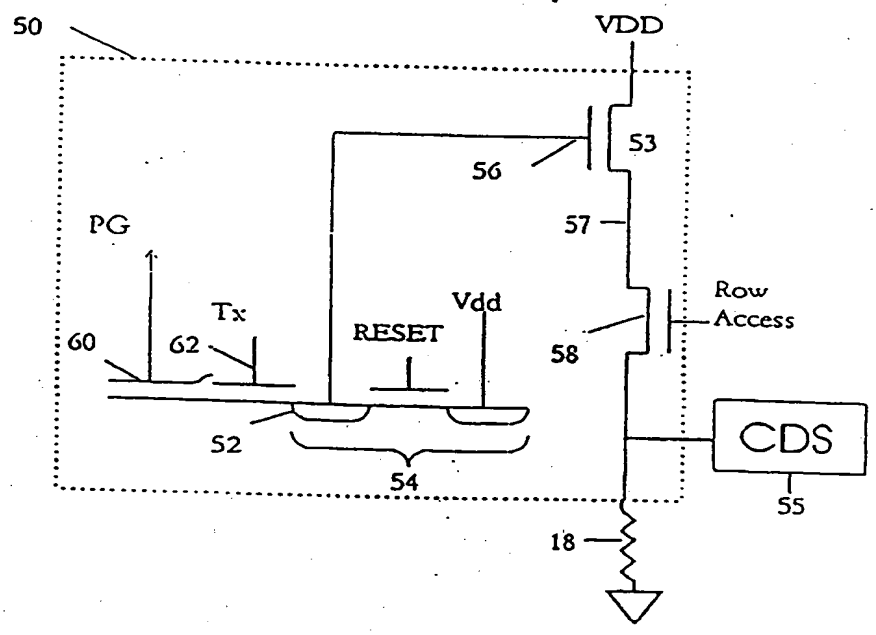


Figure 1
Prior Art

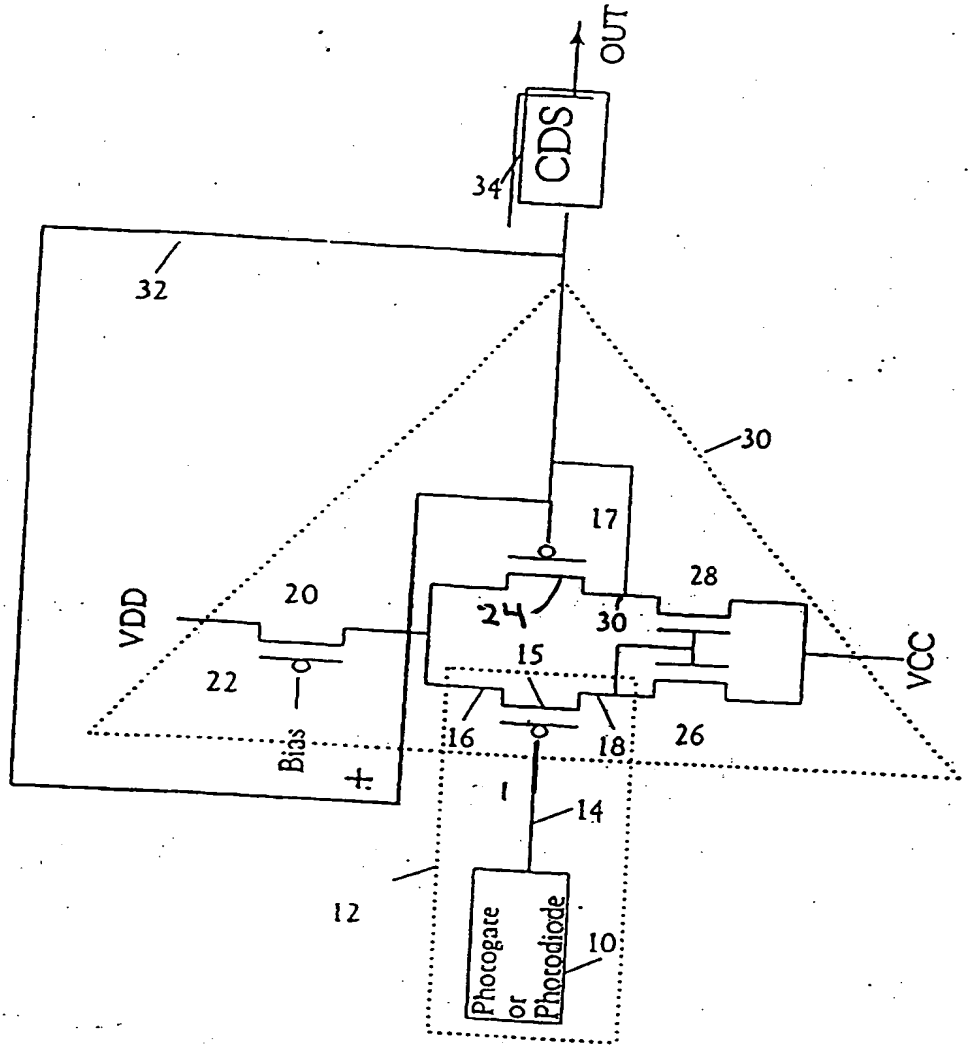

OUT
CDS
34
32
30
17
28
VDD
20
24
30
15
VCC
22
16
18
26
Bias
+
1
14
12
Photogate
or
Photodiode
10

Figure 2

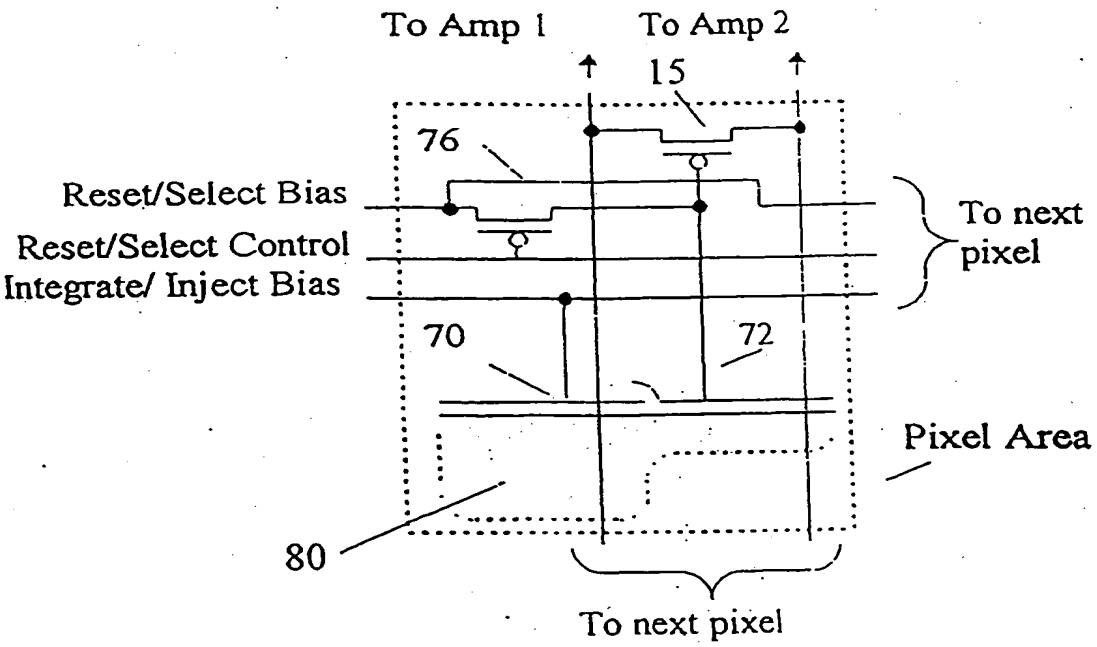

To Amp 1
To Amp 2
15
76
Reset/Select Bias
Reset/Select Control
Integrate/ Inject Bias
To next pixel
70
72
Pixel Area
80
To next pixel

Figure 3

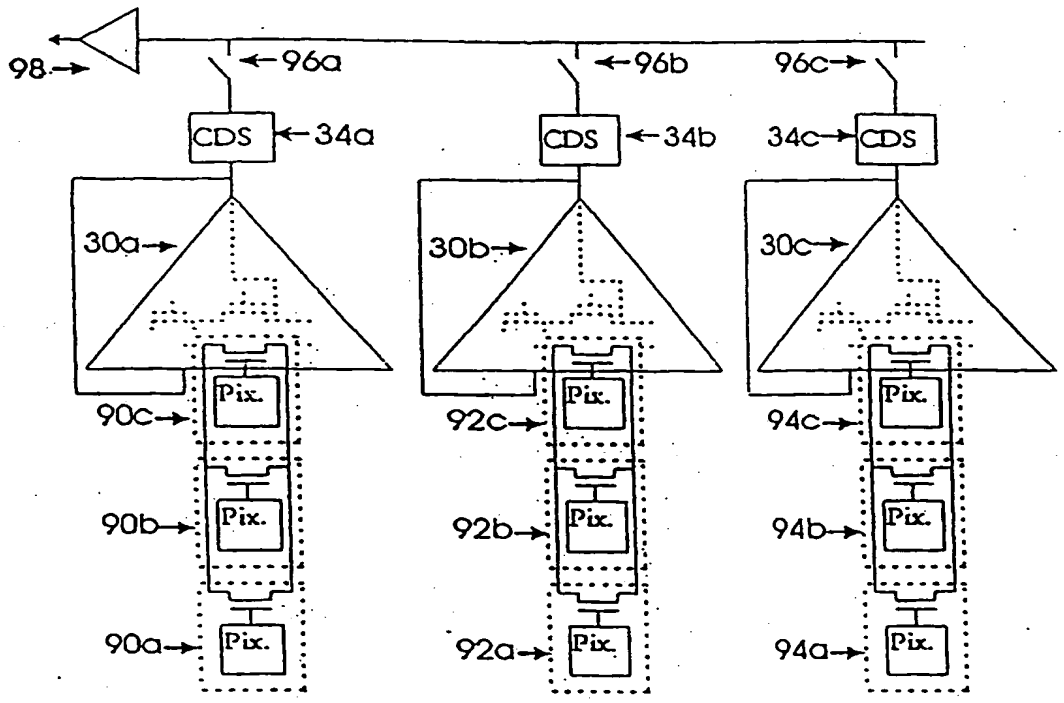
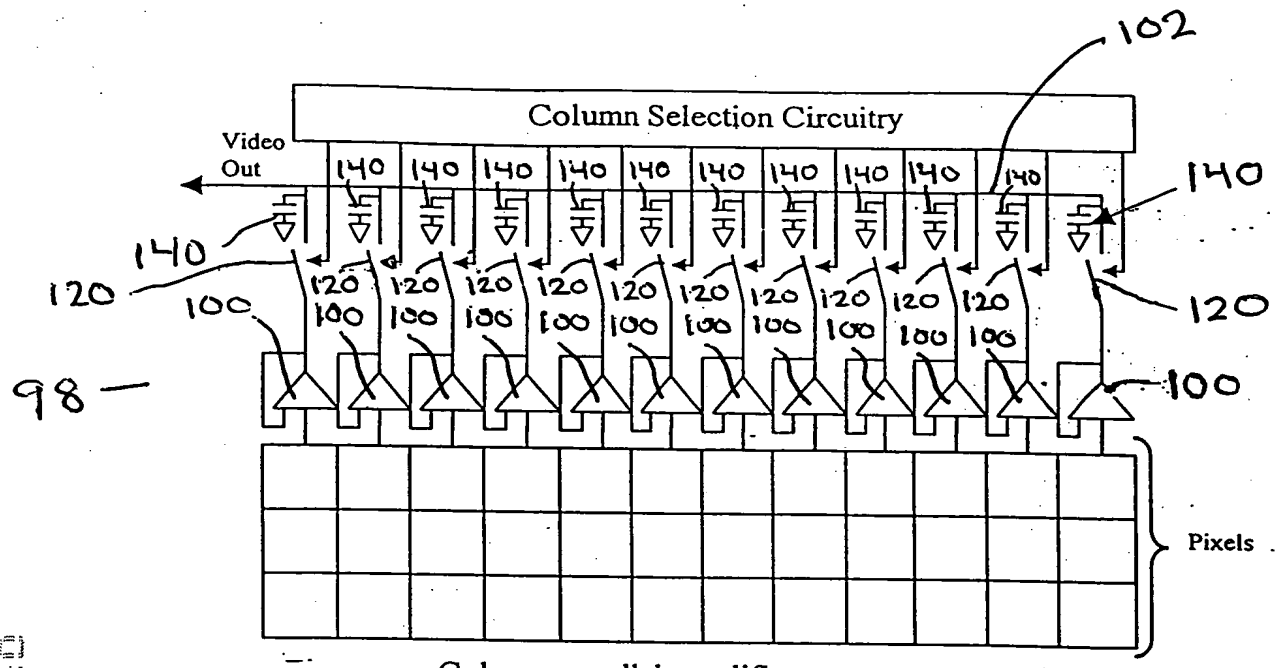

98
96a
96b
96c
CDS
34a
CDS
34b
34c
CDS
30a
30b
30c
Pix.
90c
90b
90a
92c
92b
92a
94c
94b
94a

Figure 4

Best Available Copy



Column parallel amplifiers on to common video bus.

FIGURE 5

Best Available Copy

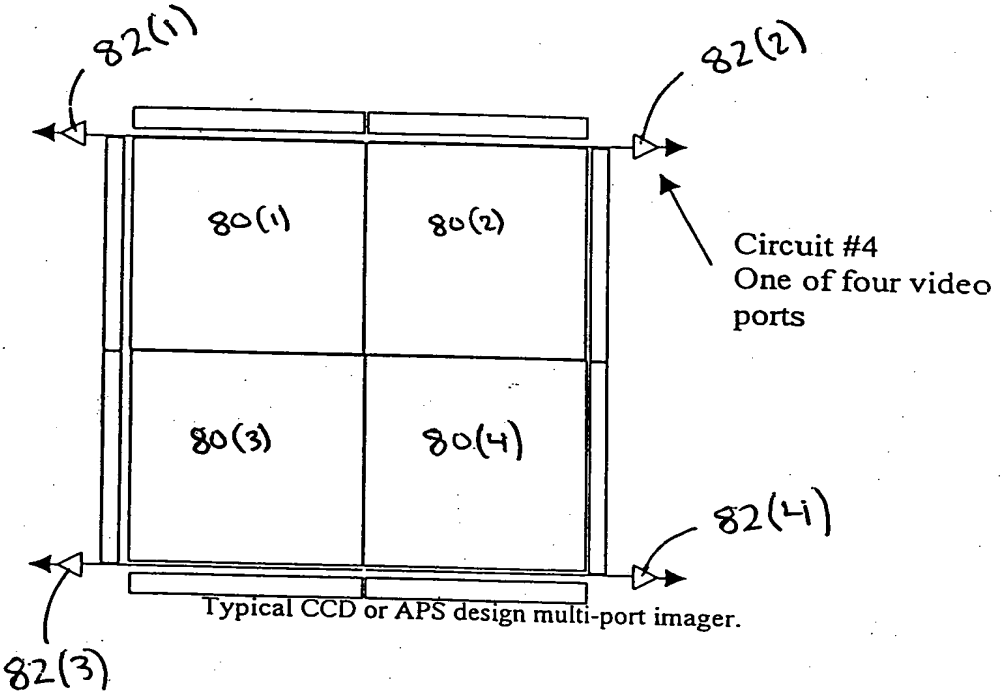
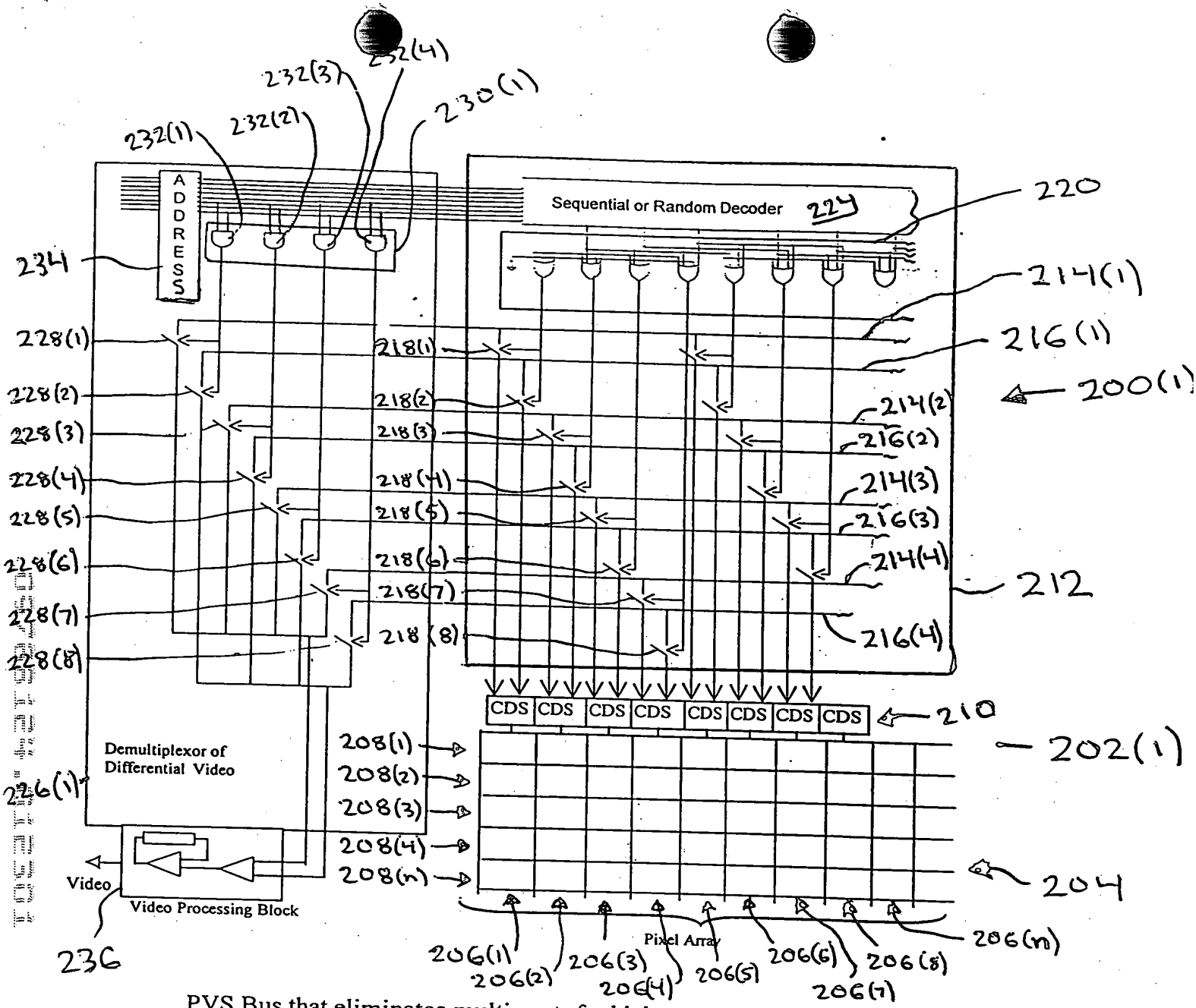


FIGURE 6



PVS Bus that eliminates multi-ports for high speed black and white

FIGURE 7A

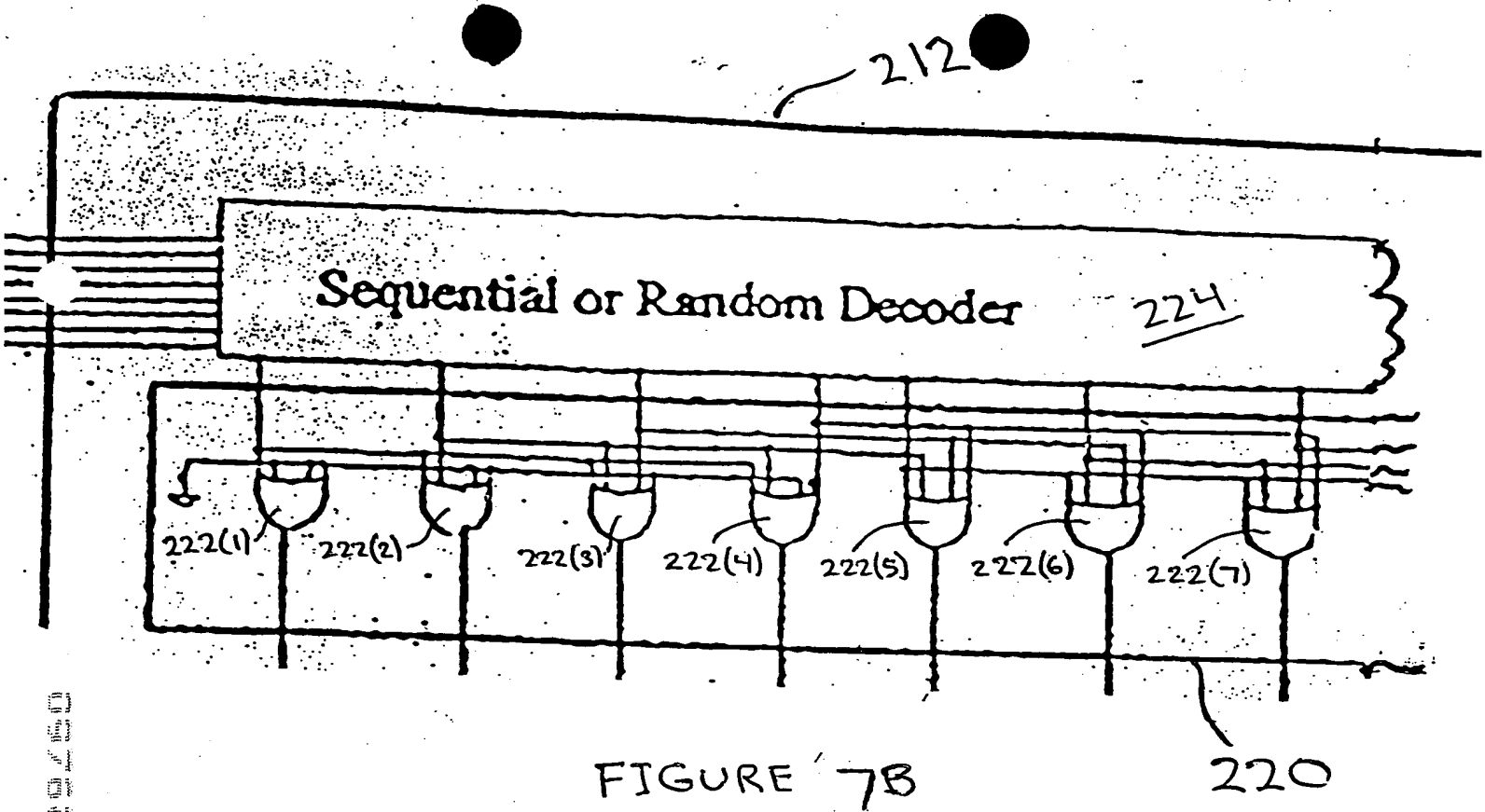


FIGURE 7B

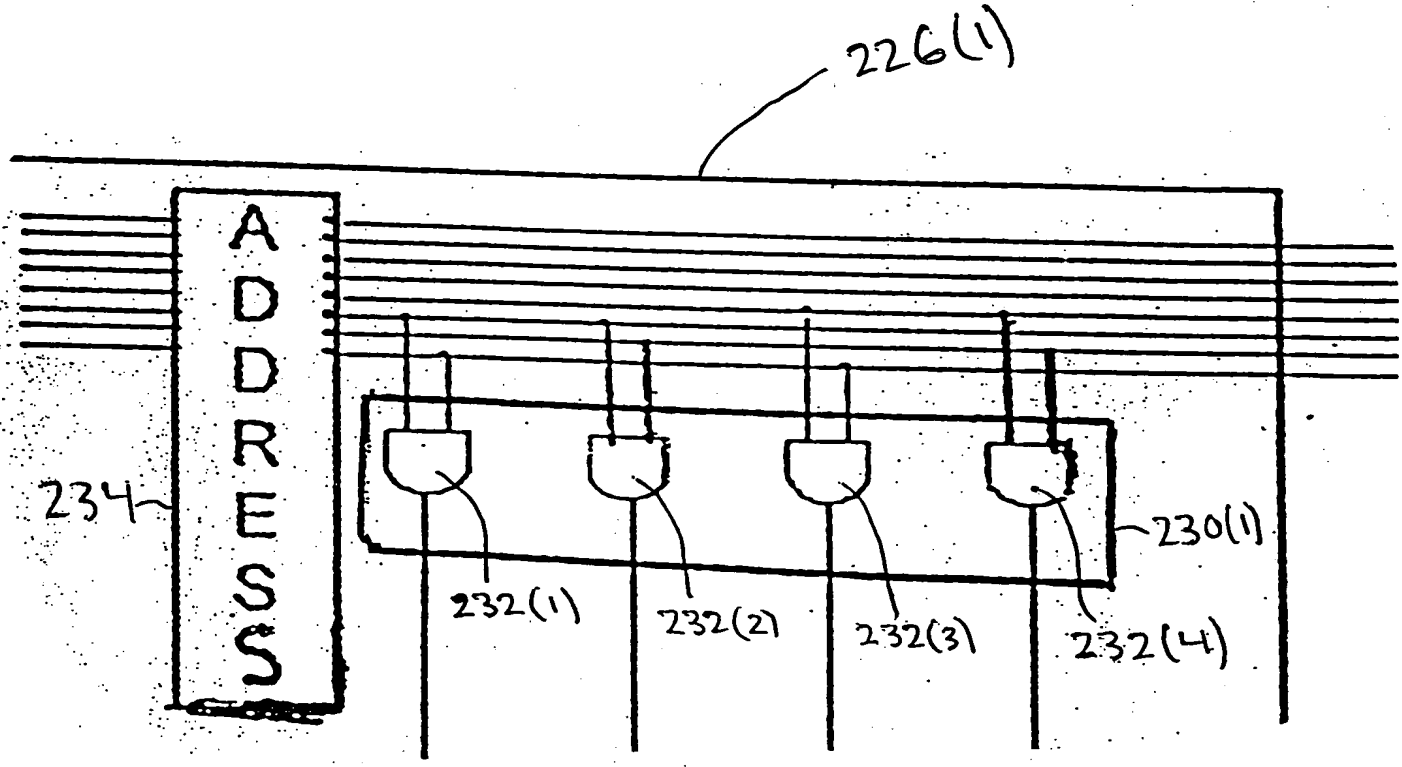
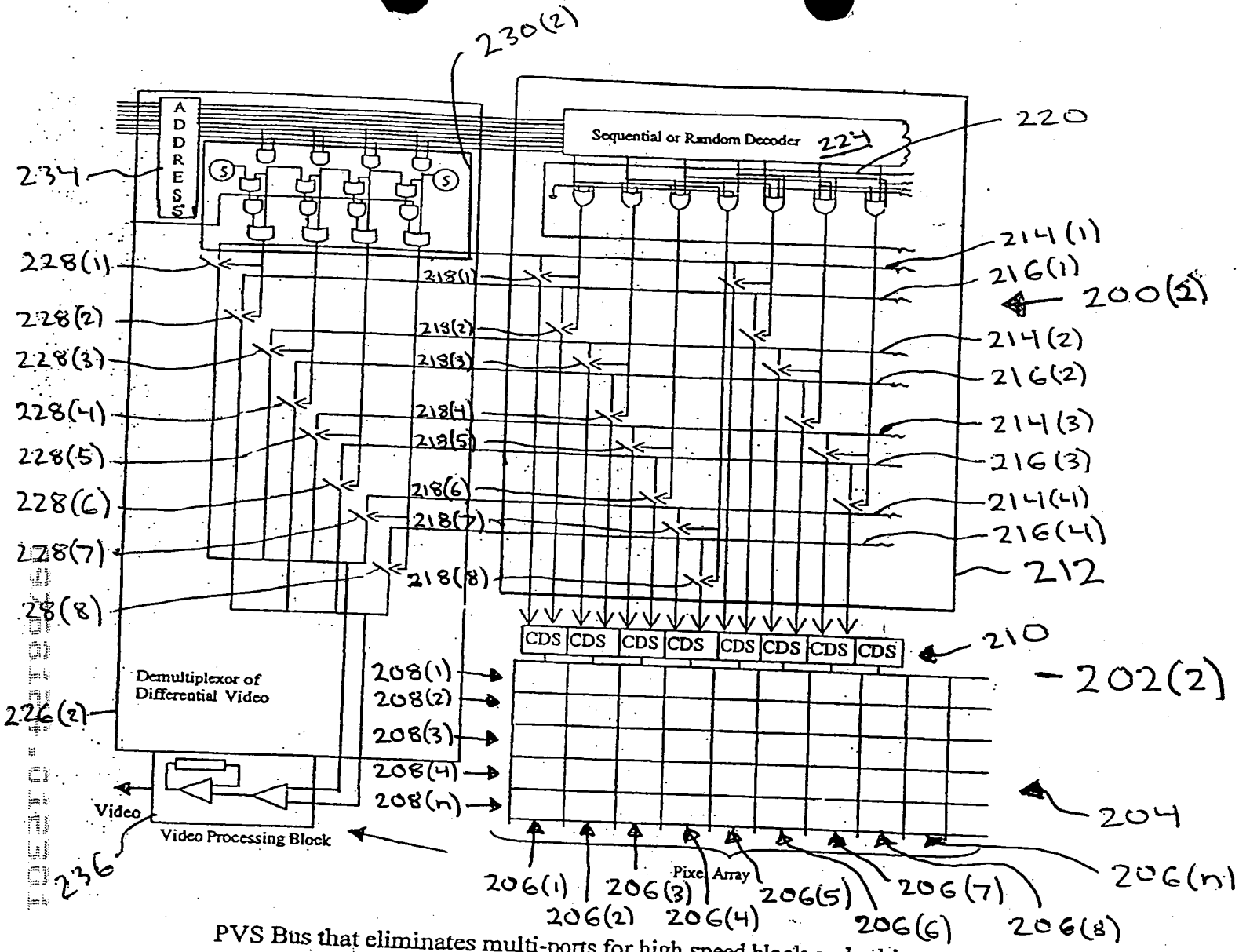


FIGURE 7C



PVS Bus that eliminates multi-ports for high speed black and white

FIGURE 8A

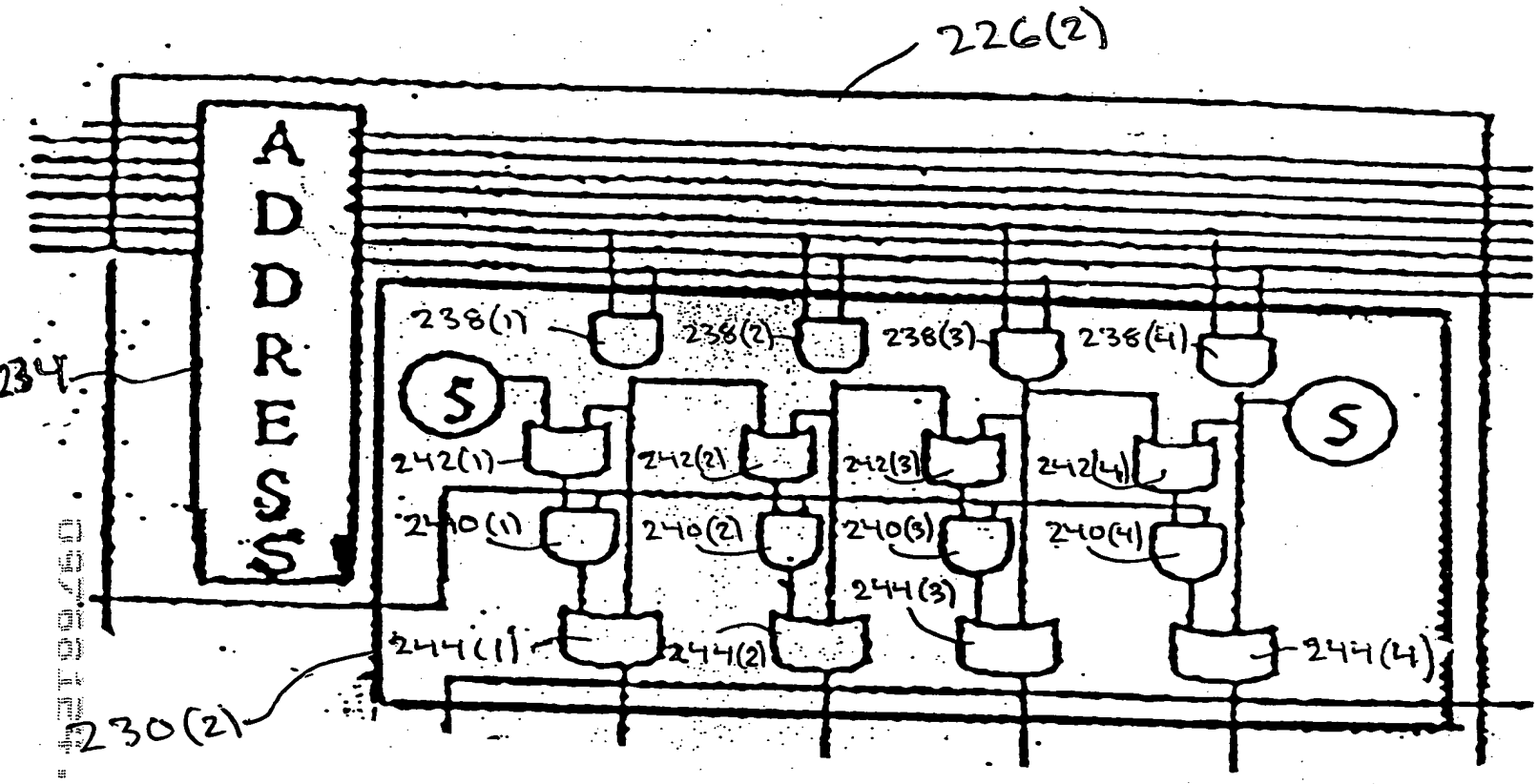
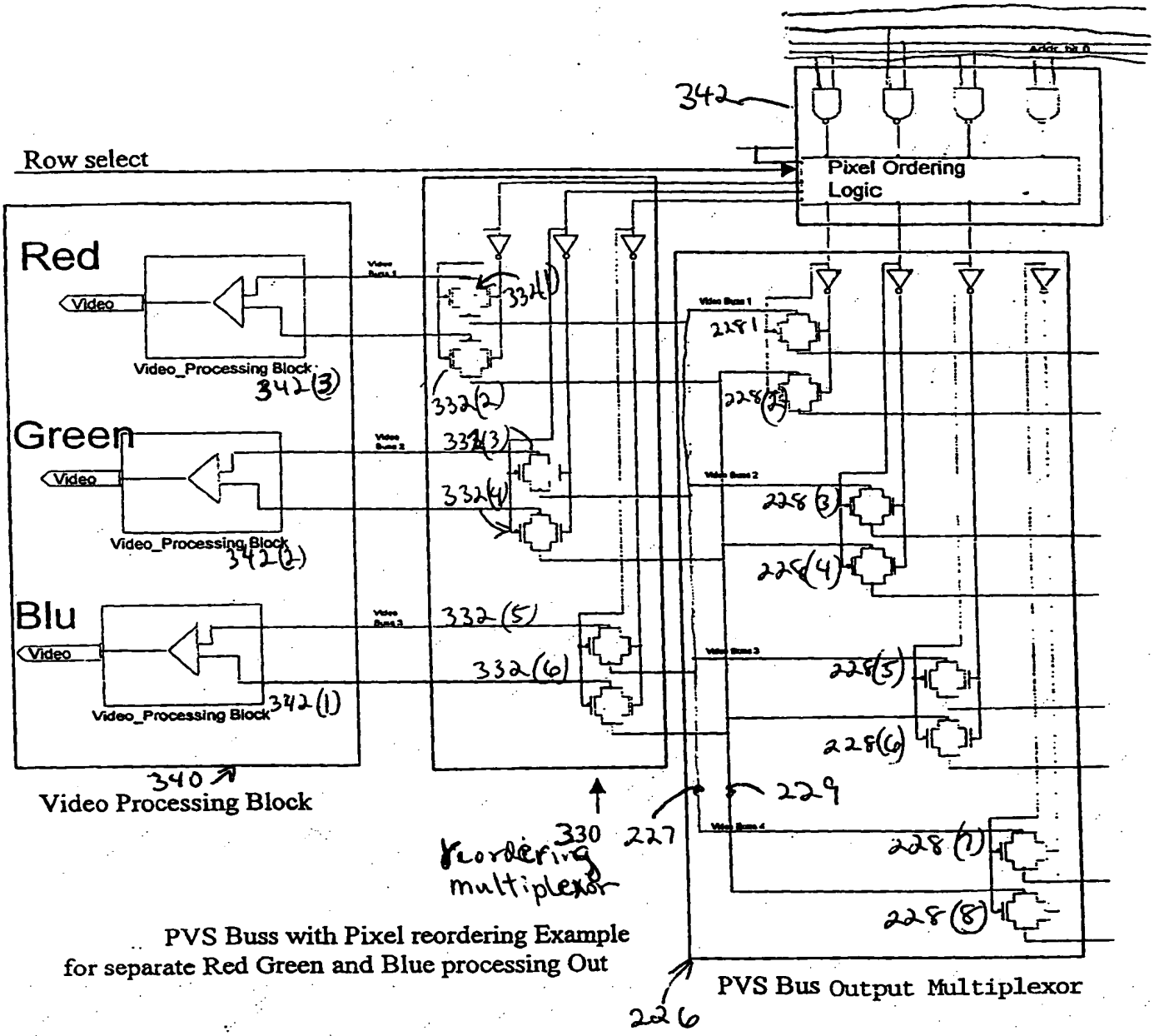

226(2)
ADDRESS
234
238(1)
238(2)
238(3)
238(4)
S
S
242(1)
242(2)
242(3)
242(4)
240(1)
240(2)
240(3)
240(4)
244(1)
244(2)
244(3)
244(4)
230(2)

FIGURE 8B



PVS Buss with Pixel reordering Example for separate Red Green and Blue processing Out

FIGURE 9